


FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 2721	SERIAL NO. 09/915,628
 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT WILLMAN, BRYAN	
		FILING DATE July 26, 2001	GROUP 2127

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO*

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

KT	AA	Frailley et al. "Software Support for Modern Microprocessor Memory Systems." Midcon Midwest Electron Show Dec 12-14 1978, PP 1-15.
KT	AB	Blumrich, et al. "Virtual-Memory-Mapped Network Interfaces." February 1995 IEEE Micro, PP 21-28.
KT	AC	Tanner, Daniel J. "Clearing Up The Confusion: Virtual vs. Mapped Memory." Computer Design, Oct 1976, PP101-105.
KT	AD	Teller, et al. "Locating Multiprocessor TLBs at Memory." Proceedings of 27 th Hawaii International Conference on Systems Sciences, 4-7 Jan. 1994, PP 554-63.
KT	AE	Szytyer, et al. "A New Virtual-Memory Implementation for UNIX." Usenix Association Summer Conference Proceedings, 9-13 June 1986, PP 81-92.
KT	AF	Baskett, F. "Pascal and Virtual Memory in a Z8000 or MC6800 Based Design Station." Compcon '80 VLSI: New Architectural Horizons, 25-28 Feb. 1980, PP 25-28.
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KT	AH	Kaplinsky, Cecil. "Memory Access Paths: Hierarchies and Protection." Wescon Conference 1984, PP 1-7
KT	AI	Kotz et al. "The Expected Lifetime of Single-Address-Space: Operating Systems." Sigmetrics 94, PP 161-170.
KT	AJ	Kuei et al. "Efficient Index Generation for Compiling Two-Level Mappings in Data-Parallel Programs." Journal of Parallel and Distributed Computing, Vol. 60, no.2; Feb 2000, PP 189-216.

2721 IDS 1449 form

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6/20/05